

1 **CLAIM OF PROVISIONAL APPLICATION RIGHTS**

2 This application is a continuation application under 37 C.F.R § 1.53(b) of application
3 Serial No. 09/136,207, filed August 19, 1998, and claims the benefit of United States Provisional
4 Patent Application No. 60/079,508 filed on March 26, 1998.

5 **BACKGROUND OF THE INVENTION**

6 Field of the Invention

7 The present invention relates generally to digital computers and, more particularly, to a
8 digital computer adapted for low power operation while playing a CD-ROM.

9 Description of the Prior Art

10 Portable computers (i.e., notebook, laptop, palmtop and the like) from major original
11 equipment manufacturers such as Toshiba, Compaq, Dell, IBM and others offer CD-ROM drives
12 as either standard or optional devices. Notebook, laptop, palmtop computers are aimed at the
13 mobile computer user who needs or wants to take work home from the office or on a business
14 trip. An added benefit of CD-ROM equipped portable computers is the opportunity to enjoy
15 periods of relaxation and pleasure by playing audio tracks from standard music CDs. In the
16 ensuing discussion, the term notebook computer will be understood to apply also to laptop,
17 palmtop and other portable, battery powered computers.

18 The Windows operating system's media player or third party audio application can play
19 back standard audio CDs on a portable computer. However the simple function of playing an
20 integral audio CD-ROM requires that the entire notebook system be powered for the duration of
21 the audio play back. This causes excessive drain on the notebook's battery power system,
22 unnecessarily consuming battery energy better saved for CPU intensive use such as word
23 processing and spreadsheet analysis.

1 Conventional laptop and notebook computers typically have several power down modes.
2 They can be powered down such that the CPU is almost completely off, with the state of the
3 CPU saved on a hard drive. A very low power portion of the CPU or an auxiliary circuit (e.g.
4 keyboard controller) is typically used to recognize when a key is pressed. The system then
5 reactivates normal power to allow the CPU to retrieve the stored machine state from the hard
6 drive thereby restoring the computer into an operating mode. Some well known power saving
7 modes are called sleep mode, suspend mode and the like.

8 Consequently, a modern energy efficient computer will, over time, operate in several
9 different power management regimes. For example, if a portable computer is being used in an
10 office environment where electrical power consumption is an insignificant concern, then the
11 computer user may want the computer to provide the highest performance and availability
12 possible. Conversely, if the computer is being operated on battery power where there is no
13 convenient source of electrical energy, then the computer user may want to choose a power
14 management regime for the computer that will maximize the time the computer operates without
15 recharging its batteries, even though performance and availability may be noticeably reduced.

16 To facilitate controlling electrical power consumption in personal computers, Intel
17 Corporation, Microsoft Corporation, and Toshiba Corporation have jointly established an
18 Advanced Configuration and Power Interface Specification ("ACPI Specification"). The ACPI
19 Specification Revision 1.0 of December 22, 1996, Copyright 1996 Intel Corporation, Microsoft
20 Corporation, Toshiba Corporation, establishes both a set of five (5) Global System States G3 -
21 Mechanical Off, G2/S5 - Soft Off, G1 - Sleeping, G0 - Working, S4 - Non-Volatile Sleep, and a
22 set of four (4) Device Power States D0 - Fully On, through D3 - Off. The ACPI Specification
23 defines the Global System States as follows.

1 G3 Electrical power is mechanically turned off.

2 G2/S5 Electrical power is turned on but the computer consumes a minimal amount of
3 power by not executing either user or system computer programs, and the
4 system's context is not preserved by hardware.

5 G1 Electrical power is turned on, the system's context is preserved by hardware or
6 system software, but user computer programs are not being executed.

7 G0 Electrical power is turned on and user computer programs are executed. In the G0
8 state, devices such as hard disk drives, CD-ROM drives, floppy diskette drives,
9 etc are dynamically turned on and off as needed.

10 S4 Electrical power may either be turned off, i.e. Global State G3, or turned on with
11 the computer consuming a minimal amount of power, i.e. Global State G2/S5,
12 while system context is preserved in a non-volatile storage file before entering
13 either the G3 or G2/S5 state, thereby permitting the computer to be restored to its
14 prior operating state, i.e. G1 or G0.

15 The ACPI Specification further defines Device Power States as follows.

16 D0 The device is completely active and responsive, and consumes the most electrical
17 power.

18 D1 A lower power state that is defined for different types of devices which preserves
19 more device context than the yet lower power state D2.

20 D2 An even lower power state than D1 that is again defined for different types of
21 devices, and which preserves less device context than state D1.

22 D3 Electrical power is fully removed from the device, device context is lost, and
23 system software must reinitialize the device when it is turned on again.

1 The different computer operating modes and associated power management regimes
2 described above are each characterized by a unique power demand (i.e., current drain) from the
3 battery power supply. This is an important feature both in design of portable computer systems,
4 and in marketing them as well. Great attention is focused on minimizing the power demand for
5 each of the different Global and Device operating modes. Thus, the power demand
6 characterizing each power management regime is a critical factor to be considered for portable
7 computers, particularly one that includes a CD-ROM drive for playing audio CDs.

8 In implementing conventional computer power management strategies, a power
9 management routine ("PMR") executed by the CPU must periodically monitor peripheral devices
10 to assess whether a peripheral device's operation may be suspended. Similarly, if it becomes
11 necessary to access a peripheral device whose operation has been suspended such as in Device
12 Power modes D1-D3, the PMR must resume that peripheral device's operation. Generally,
13 suspending the operation of a peripheral device and resuming its operation respectively require
14 that the PMR executed by the CPU perform a unique sequence of operations in turning off
15 electrical power to a peripheral device, and in turning electrical power back on. Writing a
16 computer program that detects a need to execute a power-on or a power-off sequence of
17 operations for a peripheral device is a cumbersome task.

18 Previous portable computers that include a CD-ROM use PMR functions to minimize
19 battery drain. However, if CPU operation has been suspended to save electrical power, the
20 computer can essentially do nothing. Therefore, in the minimal power drain mode, the CPU
21 cannot use the windows operating system's media player or third party audio application to play
22 audio CDs.

1 A significant power drain in portable computers occurs in the conventional LCD monitor.
2 Typically, 60 to 70% of the power consumed by a notebook is consumed by the display. Thus
3 even if a computer's devices, including even perhaps the CPU, were in a lower power state, i.e.,
4 one of the lower Device Power States D1-D3 for power savings during CD-ROM play only, the
5 need to use the normal LCD to display CD-ROM status and the music playing status would itself
6 impede significantly reducing power consumption.

7 For the reasons described above, it is apparent that a disadvantage of present portable
8 computers for playing audio CDs is that some portion of the computer system must remain
9 energized state to detect key actuation and then to restore power or activate a power restore
10 function of the CPU and associated peripherals (e.g. hard drive, keyboard controller, display,
11 etc.). At times when a portable computer is being used during travel, or when line power is
12 otherwise unavailable, the user may wish to play some audio CDs. Given the limited battery life
13 of most portables, e.g., 3 to 5 hours of use, the user may have to choose to forego using the CD-
14 ROM capability for very long, out of fear that the notebook will not be functional for needed
15 work or communication.

16 **BRIEF SUMMARY OF THE INVENTION**

17 The present invention extends the playing time for a CD-ROM equipped notebook
18 computer while minimizing the loss of potential operating time as a computer.

19 Another advantage is to select and control the music being played without engaging
20 (powering on) the CPU or other notebook peripherals, i.e., hard drive, display, memory and the
21 like.

22 Another advantage is to extend playing time and lower power drain while minimizing the
23 requirements for additional software drivers.

1 Another advantage would be to provide CD-ROM/music status to the user without using
2 the normal display screen with its typically high battery drain requirements.

3 Another advantage of the present invention is that it facilitates portable computer product
4 differentiation by providing various different audio CD playing user interfaces.

5 Another advantage of the present invention is that it permits designers of portable
6 computer systems to choose among various different audio CD playing user interfaces.

7 Another advantage of the present invention is that all of the preceding advantages may be
8 obtained merely by inserting an IC in accordance with the present invention into an existing
9 portable computer design.

10 In one embodiment the present invention is a digital computer that includes both a
11 computer subsystem and a CD-ROM subsystem. The computer subsystem is conventional and
12 includes a digital computer bus via which various digital computer devices included in the
13 computer subsystem exchange commands and data. Devices included in the computer subsystem
14 include a central processing unit ("CPU"), a random access memory ("RAM"), a display, a read-
15 write mass storage device, a manual input device, and a digital-audio generating integrated
16 circuit ("IC").

17 The CD-ROM subsystem includes a conventional CD-ROM drive and an audio output
18 amplifier that is coupled to the CD-ROM drive for receiving an analog audio signal from the CD-
19 ROM drive. The CD-ROM subsystem also includes several CD-ROM control buttons for
20 controlling operation of the CD-ROM drive during replay of audio compact disks ("CDs"). An
21 audio-interface IC, also included in the CD-ROM subsystem, is coupled to the digital computer
22 bus of the computer subsystem, to the CD-ROM drive, and to the CD-ROM control buttons. The
23 audio-interface IC, in a first operating mode in which the computer subsystem is energized and

operating, relays commands and data between the digital computer bus of the computer subsystem and the CD-ROM drive. In a second operating mode in which the computer subsystem is not energized and is inoperative, the audio-interface IC autonomously responds to signals received from the CD-ROM control buttons and transmits commands to the CD-ROM drive which cause the CD-ROM drive to play an audio CD present in the CD-ROM drive.

In a particularly preferred embodiment the present invention the audio-interface IC has a third operating mode in addition to the two described above. In this third operating mode the computer subsystem is energized and operating, and the audio-interface IC receives commands from the CD-ROM control buttons and stores such commands for subsequent retrieval by a computer program executed by the CPU included in said computer subsystem. Furthermore, in this third operating mode, as directed by a computer program executed by the CPU, the audio-interface IC either merely relays commands and data between the computer subsystem's digital computer bus and the CD-ROM drive, or independently responds to CD-ROM button commands by generating CD-ROM commands internally, and independently transmitting such commands to the CD-ROM drive to control playing an audio CD present in the CD-ROM drive.

An advantage of the present invention is that during playing of audio CDs the invention can double the interval during which the digital computer can operate on a particular amount of battery power.

These and other features, objects and advantages will be understood or apparent to those of ordinary skill in the art from the following detailed description of the preferred embodiment as illustrated in the various drawing figures.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram depicting a digital computer in accordance with the present invention having both a computer subsystem and a CD-ROM subsystem that includes an audio-interface IC;

FIG. 2 is a block diagram depicting an audio-interface IC in accordance with the present invention;

FIG. 3 is a register diagram depicting contents of a register block included in the audio-interface IC illustrated in FIG. 2;

FIG. 4 is a state diagram depicting operation of the audio-interface IC illustrated in FIG. 2 if the computer subsystem is not energized and is inoperative;

FIG. 5 is a flow diagram depicting operation of the computer together with the audio-interface IC illustrated in FIG. 2 when the computer subsystem is energized and the audio-interface IC operates in the third operating mode; and

FIG. 6 is a block diagram depicting a circuit preferably included in the audio-interface IC for interfacing between a bus included in the computer subsystem of the digital computer and a CD-ROM drive.

DETAILED DESCRIPTION

FIG. 1, depicts a system block diagram of an exemplary battery-powered portable computer 100 adapted for reading digital computer data from a compact disk - read only memory ("CD-ROM"), and for playing audio CDs. For purposes of playing audio CDs and power management, the computer 100 is functionally partitioned into a computer subsystem 104 and a CD-ROM subsystem 106.

The Computer Subsystem 104

The computer subsystem 104 includes all conventional data-processing components such as a microprocessor-based central processing unit and random access memory ("RAM") system ("CPU system") 120 together with various ICs described in greater detail below. The computer 100 also conventionally includes a display 110 (TFT LCD matrix display, CRT and the like), manual input devices 112 (e.g. keyboard, mouse, touch-pad), and read-write mass storage device 114 (e.g. hard drives, floppy drives, optical drives and the like), which operate conventionally within the computer 100.

The computer subsystem 104 includes associated input/output ("I/O") buses (e.g. PCI bus 116, and ISA bus 118) for interconnecting various subsystems included in the computer 100. In the computer 100, the CPU 120 (e.g. a Pentium microprocessor) exchanges data with the PCI bus 116 through a system controller IC 122 (e.g. Intel 82439HX known as the "Northbridge") for controlling on-board L2 cache. The system controller IC 122 is described in a document, a copy of which may be retrieved from http://developer.intel.com/design/intarch/TOP_800.HTM, that is hereby incorporated by reference. Via suitable adapter devices, conventionally the PCI bus 116 permits the CPU 120 to exchange data with higher performance devices such as the display 110 and the read-write mass storage device 114.

A bus bridge IC 124 (e.g. a 82371SB "Southbridge" IC) interconnects the PCI bus 116 with the ISA bus 118 and with an IDE bus 128. A description of the bus bridge IC 124 can be obtained from <http://developer.intel.com/design/intarch/embdmodl.htm> and is hereby incorporated by reference. The bus bridge IC 124 is available from Intel Corp., Santa Clara, California. Via suitable adapter devices, conventionally the ISA bus 118 permits the CPU 120 to exchange data with lower speed devices such as the manual input devices 112. However, for

particular configurations of the computer 100, the CPU 120 may exchange data with higher performance devices, such as the read-write mass storage device 114, via the ISA bus 118, or directly via the IDE bus 128.

A digital-audio generation IC 130 included in the computer subsystem 104 communicates with the CPU 120 either via the ISA bus 118, or via the PCI bus 116 as indicated by a pair of dashed lines 126. The digital-audio generation IC 130 is conventional and may be either a Maestro-1™ or Maestro-2™ marketed by ESS Technology, Inc. of Fremont, California.

As is well known to those skilled in the art, the computer programs executed by the microprocessor included in the CPU 120 of a laptop or notebook computer 100 usually include Power Management Routines (PMRs). Under appropriate operating conditions, the PMRs may place the computer 100 into one of the several power management operating modes such as those described previously for the ACPI Specification. Computer programs that place the computer 100 into one of the various power management operating modes may be prepared by one having ordinary skill in the art, and form no part of the present invention. Each of the various power management operating modes is characterized by a corresponding battery power requirement. For example, the current demand for the ACPI Specification's Device Power States may be those set forth below.

Device Power State	Battery Power Required
D0 - Fully On	I_{r1}
D1	$I_{r2} < I_{r1}$
D2	$I_{r3} < I_{r2}$
D3 - Off	$I_{r4} < I_{r3}$

The CD-ROM Subsystem 106

The CD-ROM subsystem 106 includes an audio-interface IC 102 in accordance with the present invention, a CD-ROM drive 138, CD-ROM control buttons 142, an icon liquid crystal display ("LCD") 144, a track-number display 147, an audio output amplifier 146, and audio output transducer 148, e.g. speakers or headphones. The CD-ROM control buttons 142, which connect to audio-interface IC 102 via a control-button bus 143, include buttons for playing or pausing an audio CD, for fast-forwarding the audio CD, for rewinding the audio CD, and for stopping or ejecting the audio DC. Thus, the CD-ROM control buttons 142 permit a user of the computer 100 to completely control operation of the CD-ROM drive 138 while playing an audio CD. The CD-ROM drive 138 is a conventional CD-ROM drive capable of operating with conventional ATAPI interface commands provided through an IDE-bus extension 129 which originates at the audio-interface IC 102. The CD-ROM subsystem 106 also includes an audio signals bus 152 that supplies left and right channel stereo audio signals directly to the audio output amplifier 146.

Depending upon the operating mode of the computer 100, an audio switch 154, which operates in response to control signals received from the audio-interface IC 102, may couple the left and right channel stereo audio signals to the digital-audio generation IC 130 included in the computer subsystem 104. When the computer subsystem 104 is not energized, to reduce electrical power consumption caused by leakage electrical currents in ICs included in the computer subsystem 104:

1. the audio switch 154 electrically isolates the audio signals bus 152 from the CD-ROM drive 138; and

2. the audio-interface IC 102 correspondingly electrically isolates itself from the IDE bus 128.

A loudness control-signal line 156 couples a volume control signal from the audio-interface IC 102 to the audio output amplifier 146. As described in greater detail below, the audio-interface IC 102 provides a signal to the icon LCD 144 via a LCD-signal line 145 for indicating that the CD-ROM drive 138 is operating. The audio-interface IC 102 provides signals to the track-number display 147 via a track-number-display bus 149 for displaying a track number as an audio CD is played.

A System Management Bus ("SMBus") 162 permits the audio-interface IC 102 to exchange commands and data with the computer subsystem 104. Within the computer subsystem 104, the SMBus 162 connects to the ISA bus 118 via a keyboard controller IC 164 as indicated in FIG. 1, or the SMBus 162 may connect directly to the bus bridge IC 124, as indicated by a dashed line 166 in FIG. 1.

Both the computer subsystem 104 and the CD-ROM subsystem 106 receive electrical power directly from a battery, not illustrated in any of the FIGS. Depending upon the operating mode of the computer 100 for playing audio CDs, either the CD-ROM subsystem 106 alone, or both the computer subsystem 104 and the CD-ROM subsystem 106 may be energized. If the computer subsystem 104 receives no electrical power, then operation of the CD-ROM drive 138 is effected completely within the CD-ROM subsystem 106 with the audio-interface IC 102 originating signals for controlling operation of the CD-ROM drive 138. If the computer subsystem 104 is energized and operating, then operation of the CD-ROM drive 138 can be effected, via the audio-interface IC 102, by commands received from a computer program executed by the 120.

Interconnections Between The Computer Subsystem 104 and The CD-ROM Subsystem 106

The audio-interface IC 102 exchanges commands and data with a computer program executed by the CPU 120 through the IDE bus 128 and the SMBus 162. The computer subsystem 104 also selectively supplies left and right audio signals from the CD-ROM drive 138 to the digital-audio generation IC 130 depending upon the operating mode of the computer 100. During operation of the computer 100 in which the computer subsystem 104 is energized and operating, the audio-interface IC 102 relays commands and data between the computer subsystem 104 and CD-ROM drive 138. Commands and data which the computer subsystem 104 exchanges with the CD-ROM drive 138 are well known to those skilled in the art.

General Description of The Audio-Interface IC 102

Referring now to a detailed block diagram of the audio-interface IC 102 depicted in FIG. 2, the audio-interface IC 102 includes a state machine 202 which controls overall operation of the audio-interface IC 102. Inclusion of the state machine 202 in the audio-interface IC 102, as contrasted with a programmable controller, provides better performance. The state machine 202 connects via an internal bus 204 to a SMBus interface 206, to a register block 208, to a LCD control 212, and to a clock generator 214.

The SMBus interface 206, which is coupled to the SMBus 162, issues an interrupt signal from an INTN pin 222 whenever a user of the computer 100 presses any of the CD-ROM control buttons 142. The INTN pin 222 may be advantageously connected to a SMBus alert signal-line of the SMBus 162. Upon receiving a SMBus alert signal, a computer program executed by the CPU 120 can interrogate the register block 208 included in the audio-interface IC 102 via the SMBus 162 to determine which of the CD-ROM control buttons 142 has been pressed.

1 In addition to being coupled to the internal bus 204, the register block 208 is also coupled
2 to an operating-mode bus 226 by which the computer 100 supplies operating mode signals to the
3 audio-interface IC 102 via the operating-mode bus 226 include a reset signal which upon
4 activation causes the audio-interface IC 102 to be reset. The operating-mode bus 226 also
5 supplies a signal to the audio-interface IC 102 that indicates whether the computer subsystem
6 104 is energized. And, the operating-mode bus 226 supplies a signal to the audio-interface IC
7 102 which indicates if the device connected to the IDE-bus extension 129 is a CD-ROM drive.
8 Supplying a signal to the audio-interface IC 102 which indicates whether a CD-ROM drive is
9 connected to the IDE-bus extension 129 avoids a requirement that the audio-interface IC 102 first
10 power on and then interrogate a device connected to the IDE-bus extension 129 to determine
11 whether such device is a CD-ROM drive or a hard disk drive. Avoiding this interrogation
12 requirement is particularly advantageous if the computer 100 permits a user to readily
13 interchange a CD-ROM drive for some other device such as a hard disk drive or conversely.

14 As is readily apparent, the CD-ROM control buttons 142, icon LCD 144 and track-
15 number display 147 of the CD-ROM subsystem 106 provide a user of the computer 100 with a
16 self-contained interface for playing audio CDs. To effect this functionality, the audio-interface
17 IC 102 includes control-button logic 232 that receives electrical signals from the CD-ROM
18 control buttons 142 via the control-button bus 143. In response to such signals, the control-
19 button logic 232 may store data into the register block 208, or it may cause a digital volume
20 control 236 to transmit control signals via the loudness control-signal line 156 to the audio
21 output amplifier 146. Similarly, when the computer subsystem 104 is playing an audio CD the
22 LCD control 212 transmits a signal via the LCD-signal line 145 to activate an audio playback
23 icon included in the icon LCD 144. And during such audio CD playback the LCD control 212

1 transmits signals via the track-number-display bus 149 which cause the track-number display 147
2 to display a number that indicates the current track of an audio CD. As described in greater
3 detail below" data indicating the current track number is present in the register block 208.

4 When the computer subsystem 104 is energized, a host IDE interface 242 included in the
5 audio-interface IC 102 couples electrical signals between the IDE bus 128 and the IDE-bus
6 extension 129 via an IDE-signals multiplexer 244 included in the audio-interface IC 102. The
7 electrical signals coupled between the IDE bus 128 and the IDE-bus extension 129 effect
8 exchanges of commands and data between a computer program executed by the CPU 120 and the
9 CD-ROM drive 138. However, if only the CD-ROM subsystem 106 is energized, then the state
10 machine 202 exchanges electrical signals with the IDE-bus extension 129 via the IDE-signals
11 multiplexer 244. And as set forth above, if the computer subsystem 104 is not energized the host
12 IDE interface 242 electrically isolates the audio-interface IC 102 from the IDE bus 128 of the
13 computer subsystem 104 to reduce, as much as possible, leakage current flowing from the CD-
14 ROM subsystem 106 into the computer subsystem 104. Set forth below is a table listing
15 commands in accordance with the ATAPI protocol which the state machine 202 may transmit to
16 the CD-ROM drive 138 via the IDE-signals multiplexer 244 and IDE-bus extension 129.

Command	Opcode
Pause / Resume	4Bh
Play Audio MSF	47h
Stop	1Bh
Read Table of Contents	43h
Request Sense	03h
Read Sub Channel	42h
Test Unit Ready	00h
Lock/Unlock	1Eh

Inquiry	12h
Sleep	E6h

The state machine 202 also transmits a pair of signals from the audio-interface IC 102 via a mode-control signal bus 246. A mode signal supplied by the state machine 202 to the mode-control signal bus 246 indicates that the audio-interface IC 102 is operating for receiving signals from the CD-ROM control buttons 142. An electrical power control signal supplied by the state machine 202 to the mode-control signal bus 246 indicates either that the CD-ROM drive 138 is or has been recently operating, or that the CD-ROM drive 138 has not been operating recently. This signal may be used within the CD-ROM subsystem 106 for appropriately controlling the supply of electrical power to the CD-ROM drive 138 and/or the audio output amplifier 146.

During power-on initialization of the computer 100, input signals supplied to the audio-interface IC 102 via the mode-control signal bus 246 respectively select an address for the audio-interface IC 102 on the SMBus 162, and also control whether the state machine 202 transmits the signal for controlling the supply of electrical power to the CD-ROM drive 138 and/or the audio output amplifier 146.

The audio-interface IC 102 also receives a pair of signals via a mode-set signal bus 248 that specify particular hardware characteristics of the CD-ROM subsystem 106. One of these signals specifies which one of two (2) different types of volume control ICs is included in the audio output amplifier 146. The other mode-set signal specifies a particular characteristic for signals transmitted from the audio-interface IC 102 to the track-number display 147 via the track-number-display bus 149.

The clock generator 214 included in the audio-interface IC 102 connects to an oscillator-in signal-line 252 and to an oscillator-out signal-line 254. The oscillator-in and -out signal-lines

252 and 254 connect to an 8 MHz crystal external to the audio-interface IC 102 that is not separately depicted in any of the FIGs.

Register Block 208

FIG. 3 illustrates registers 208a-208h included in the register block 208. Seven high order bits of a low-order byte 208aa of a Chip and Revision ID Register 208a stores a programmable address for the audio-interface IC 102 on the SMBus 162. A high-order byte 208ab of the Chip and Revision ID Register 208a stores a revision number for the audio-interface IC 102.

Respective states stored in software programmable bits 0, 1, 3, and 5 in a low-order byte of a Control-Buttons Change-Register 208b store data indicating pressing of specific CD-ROM control buttons 142. Bit 208ba0 in the stores data indicating that a rewind button has been pressed. Bit 208ba1 stores data indicating that a fast-forward button has been pressed. Bit 208ba3 in the Control-Buttons Change-Register 208b stores data indicating that a stop/eject button has been pressed. And bit 208ba5 in the Control-Buttons Change-Register 208b stores data indicating that a play/pause button has been pressed. Software programmable bit 208hb0 of a high-order byte of an Interrupt Status Register 208h stores data indicating that one of the CD-ROM control buttons 142 has been pressed, and causes the interrupt signal to be transmitted from the audio-interface IC 102 via the INTN pin 222. Because bits 0, 1, 3 and 5 in the Control-Buttons Change-Register 208b are software programmable, a computer program executed by the CPU 120 can assign data values, i.e. 0 or 1, to any of them.

A state of bit 208ba7 of the Control-Buttons Change-Register 208b indicates whether the audio-interface IC 102 is enabled for transmitting the ATAPI protocol commands listed above to the CD-ROM drive 138. A state of bit 208ca1 in a low-order byte of an Electrical Power

1 Register 208c enables the audio-interface IC 102 for controlling electrical power to the CD-ROM
2 drive 138 and to the audio output amplifier 146. If bit 208ca1 is enabled, a state of bit 208ca0 in
3 the Electrical Power Register 208c controls the supply of electrical power to the CD-ROM drive
4 138 and audio output amplifier 146.

5 Respective states stored in software programmable bits 0, 1 and 2 in a low-order byte of a
6 Command Control Register 208d store data for controlling operation of the audio-interface IC
7 102 when the computer subsystem 104 is energized. Bit 208da0 in the Command Control
8 Register 208d stores data which controls whether the audio-interface IC 102 is enabled for
9 executing a single instance of a function specified, as described above, by data values that are
10 assigned to bits in the Control-Buttons Change-Register 208b by pressing the CD-ROM control
11 buttons 142. Bit 208da1 stores data which prevents the audio-interface IC 102 from executing
12 any function specified by data values assigned by pressing the CD-ROM control buttons 142 to
13 bits in the Control-Buttons Change-Register 208b. Bit 208da2 stores data which indicates
14 whether the audio-interface IC 102 has executed a single instance of a function specified by data
15 values assigned by pressing the CD-ROM control buttons 142 to bits in the Control-Buttons
16 Change-Register 208b.

17 A bit 208ea0 in a low-order byte of an Operating Mode Register 208e stores data which
18 specifies a specific operating mode of the CD-ROM subsystem 106, i.e. the third operating
19 mode, to be described in greater detail below. When the audio-interface IC 102 operates in the
20 third operating mode, bit 208ea4 in the operating Mode Register 208e stores data which specifies
21 selection of a particular drive, i.e. a master drive or a slave drive, to receive ATAPI protocol
22 commands from the audio-interface IC 102.

Bit 208eb1 in a high-order byte of the Operating Mode Register 208e controls application of a clock signal to the state machine 202. When the audio-interface IC 102 operates in the mode in which it merely relays data and ATAPI commands between the IDE bus 128 and the IDE-bus extension 129, to conserve energy bit 208eb1 may be set thereby halting application of the clock signal to the state machine 202.

Bits 0-6 of a low-order byte 208ga of a Track Number Register 208g store a track number read from an audio CD while it is being played. Software programmable bits 0-6 of a high-order byte 208gb store a track number displayed on the track-number display 147.

Operating Modes

If the computer subsystem 104 is energized and operating, in one operating mode of the CD-ROM subsystem 106 the audio-interface IC 102 transparently relays commands and data between the IDE bus 128 and the CD-ROM drive 138. FIG. 4 depicts states and state transitions of the audio-interface IC 102 for an operating mode of the computer 100 in which the computer subsystem 104 is not energized and is inoperative. When the computer subsystem 104 is not energized and the CD-ROM subsystem 106 is initially energized, or immediately after the audio-interface IC 102 is reset by a signal received from the operating-mode bus 226, the audio-interface IC 102 enters an initialize state 302 depicted in FIG. 4.

A pressing of the play/pause button when the audio-interface IC 102 is in the initialize state 302 causes the audio-interface IC 102 to transition to a play state 304 in which the audio-interface IC 102 transmits commands in accordance with the ATAPI protocol to the CD-ROM drive 138 that cause the CD-ROM subsystem 106 to play an audio CD. If the audio-interface IC 102 is in the play state 304, then pressing the stop or eject button, or reaching the end of all the

1 audio CD tracks causes the audio-interface IC 102 to re-enter the initialize state 302 and to return
2 to the beginning of the audio CD.

3 While the CD-ROM subsystem 106 is in the play state 304 playing an audio CD, pressing
4 either the fast-forward or rewind buttons causes the audio-interface IC 102 to enter a fast-
5 forward-or-rewind state 306. In the fast-forward-or-rewind state 306 the audio-interface IC 102
6 transmits commands to the CD-ROM drive 138 that either fast-forward or rewind the audio CD.
7 If the CD-ROM drive 138 completes the fast-forward or rewind command, or reaches the end or
8 beginning of the audio CD track, the audio-interface IC 102 re-enters the initialize state 302.
9 While the CD-ROM subsystem 106 is fast-forwarding or rewinding an audio CD, pressing the
10 play button causes the audio-interface IC 102 to enter the play state 304 and resume playing the
11 audio CD at the beginning of the present track.

12 While the CD-ROM subsystem 106 is in the play state 304 playing an audio CD or in the
13 fast-forward-or-rewind state 306 fast-forwarding or rewinding an audio CD, pressing the pause
14 button causes the audio-interface IC 102 to enter a pause state 308 which pauses operation of the
15 CD-ROM drive 138. If the audio-interface IC 102 is in the pause state 308, pressing the play
16 button causes the audio-interface IC 102 to enter the play state 304 and resume playing the audio
17 CD at the present location in the track, pressing either the fast-forward or rewind buttons causes
18 the audio-interface IC 102 to enter the pause state 308, and pressing the stop button causes the
19 audio-interface IC 102 to enter the initialize state 302.

20 If the audio-interface IC 102 is in the initialize state 302, and a signal has been supplied
21 to the audio-interface IC 102 via the mode-set signal bus 248 which enables the state machine
22 202 for controlling the supply of electrical power to the CD-ROM drive 138 and/or the audio
23 output amplifier 146, and a pre-established two (2) minute interval passes during which none of

1 the CD-ROM control buttons 142 are pressed; then the audio-interface IC 102 enters a sleep state
2 312. Upon entering the sleep state 312 the audio-interface IC 102 sends an ATAPI protocol
3 sleep command to the CD-ROM drive 138 thereby slowing down a clock included in the CD-
4 ROM drive 138. If the audio-interface IC 102 is in the sleep state 312 and a second, pre-
5 established two (2) minute interval passes during which none of the CD-ROM control buttons
6 142 are pressed, then the audio-interface IC 102 enters a suspend state 314 in which the audio-
7 interface IC 102 transmits a signal via the mode-control signal bus 246 which indicates that the
8 CD-ROM drive 138 has not been operating recently. Electrical circuitry included in the CD-
9 ROM subsystem 106 may use this signal from the audio-interface IC 102 for removing electrical
10 power from both the CD-ROM drive 138 and from the audio output amplifier 146. If the audio-
11 interface IC 102 is either in the sleep state 312 or in the suspend state 314, then pressing any of
12 the CD-ROM control buttons 142 causes the audio-interface IC 102 to re-enter the initialize state
13 302.

14 In addition to the two operating modes described above, the preferred embodiment of the
15 audio-interface IC 102 may be configured to operate a third operating mode. In this third
16 operating mode the computer subsystem 104 is energized and operating, the audio-interface IC
17 102 receives commands from the CD-ROM control buttons 142, and stores such commands into
18 the Control-Buttons Change-Register 208b for subsequent retrieval by a computer program
19 executed by the CPU 120. The flow diagram of FIG. 5 depicts operation of the computer 100
20 including the audio-interface IC 102 for playing audio CDs in this third operating mode. Thus as
21 depicted in FIG. 5, in the third operating mode while a user does not press any of the CD-ROM
22 control buttons 142 the state machine 202 loops at decision block 372 waiting for one of the CD-
23 ROM control buttons 142 to be pressed. If any of the CD-ROM control buttons 142 are pressed,

1 the state machine 202 in processing block 374 sets the bit 208hb0 in the Interrupt Status Register
2 208h thereby causing the SMBus interface 206 to transmit a SMBus interrupt INTN via the
3 INTN pin 222 either to the keyboard controller IC 164, or to the bus bridge IC 124. In addition
4 to transmitting a SMBus interrupt, the state machine 202 in processing block 374 also sets the
5 appropriate bit in the Control-Buttons Change-Register 208b to indicate which of the CD-ROM
6 control buttons 142 has been pressed. The computer program executed by the CPU 120 in
7 processing block 376 responds to receipt of the INTN interrupt by reading the contents of the
8 Control-Buttons Change-Register 208b, and by then transmitting data to the CPU 120 via the
9 SMBus 162 that resets the bit 208hb0 of the Interrupt Status Register 208h thereby clearing the
10 INTN interrupt.

11 After the computer program executed by the CPU 120 clears the INTN interrupt, the state
12 machine 202 in decision block 378 determines whether the computer program executed by the
13 CPU 120 has previously set bit 208da1 in the Command Control Register 208d. If bit 208da1 is
14 not set, then the state machine 202 in decision block 382 determines whether the computer
15 program executed by the CPU 120 has previously set bit 208da0 in the Command Control
16 Register 208d. If bit 208da0 is set, then the state machine 202 in processing block 384 transmits
17 to the CD-ROM drive 138 via the IDE-bus extension 129 the ATAPI command(s) which cause
18 the IDE bus 128 to respond appropriately to the pressing of the CD-ROM control button 142. If
19 bit 208da1 has been set, and bit 208da0 has not been set, then the state machine 202 merely
20 passes through processing block 386 without sending any commands to the CD-ROM drive 138.

21 After performing either processing block 384 or processing block 386, the state machine
22 202 in processing block 392 again sets bit 208hb0 in the Interrupt Status Register 208h thereby
23 again causing the SMBus interface 206 to transmit a SMBus interrupt via the INTN pin 222

1 either to the keyboard controller IC 164, or to the bus bridge IC 124. As before, the computer
2 program executed by the CPU 120 in processing block 394 responds to receipt of the INTN
3 interrupt by transmitting data to the CPU 120 via the SMBus 162 that resets bit 208hb0 of the
4 Interrupt Status Register 208h thereby clearing the INTN interrupt. After the computer program
5 executed by the CPU 120 resets bit 208hb0, the state machine 202 returns to decision block 372
6 to resume waiting for one of the CD-ROM control buttons 142 to be pressed.

7 FIG. 6 depicts a preferred circuit for interfacing between the IDE bus 128, the IDE-bus
8 extension 129 and the audio-interface IC 102. While the host IDE interface 242 and the IDE-
9 signals multiplexer 244 of the audio-interface IC 102 may employ conventional IC I/O buffers,
10 such conventional circuits will appreciably delay signals passing through the audio-interface IC
11 102 between the IDE bus 128 and the IDE-bus extension 129. Rather than employing
12 conventional IC I/O buffer circuits for the host IDE interface 242 and the IDE-signals
13 multiplexer 244, the preferred circuit depicted in FIG. 6 employs transmission gates 402
14 interposed between the IDE bus 128 and the IDE-bus extension 129 for selectively either
15 coupling them together, or isolating them from each other. Use of the transmission gates 402
16 rather than conventional IC I/O buffers significantly reduces delay for signals passing through
17 the audio-interface IC 102 between the IDE bus 128 and the IDE-bus extension 129.

18 Analogously to the transmission gates 402, signals that the state machine 202 receives
19 from the IDE-bus extension 129 are also selectively coupled to or isolated from input buffers 404
20 for the state machine 202 by a transmission gates 406 also in response to a signal generated by
21 the state machine 202. Alternatively, signals that the state machine 202 transmits to the IDE-bus
22 extension 129 pass through output drivers 408 when the output drivers 408 are enabled by a
23 control signal supplied by the state machine 202. This preferred configuration for exchanging

1 signals between the state machine 202 and the IDE-bus extension 129 also permits isolating the
2 state machine 202 from input signals on the IDE-bus extension 129 during certain critical events
3 such as when the CD-ROM drive 138 is removed to be replaced by another device that is to be
4 coupled to the IDE bus 128 without removing electrical power from the computer subsystem 104
5 and CD-ROM subsystem 106.

6 The transmission gates 402, input buffers 404, transmission gates 406, and output drivers
7 408 collectively constitute an analog multiplexer that, in the illustration of FIG. 6, is enclosed
8 within a dashed line 412. Enabling operation of the analog multiplexer 412 permits exchanging
9 signals between the IDE-bus extension 129 and either the IDE bus 128 or state machine 202.
10 Disabling operation of the analog multiplexer 412 isolates the IDE-bus extension 129 both from
11 the IDE bus 128 and from the state machine 202. Inclusion of the analog multiplexer 412 in the
12 audio-interface IC 102 permits various IDE devices, such as the CD-ROM drive 138, a hard disk,
13 a Digital Video Disk ("DVD") drive, a ZIP drive, or a Superdisk, to be connected to or
14 disconnected from the IDE-bus extension 129 of a fully operational computer 100.

15 Operation of the analog multiplexer 412 is enabled or disabled by a signal coupled from a
16 multiplexer control 414 included in the audio-interface IC 102. The multiplexer control 414
17 transmits the control signal to the transmission gates 402 and 406 via a transmission-gate-control
18 signal-line 416. For disabling operation of the analog multiplexer 412, the multiplexer control
19 414 receives software data via the SMBus 162. For enabling operation of the analog multiplexer
20 412, the multiplexer control 414 may receive either or both a hardware signal via a touchdown
21 signal-line 418 and/or software data via the SMBus 162. The multiplexer control 414 also
22 receives signals from the state machine 202 via a multiplexer-control-signal selection-bus 422.
23 Signals supplied to the multiplexer control 414 from the state machine 202 via the multiplexer-

control-signal selection-bus 422 determine whether operation of the multiplexer control 414 is enabled by the signal supplied via the touchdown signal-line 418, or by data supplied to the multiplexer control 414 via the SMBus 162.

Accordingly, as illustrated in FIG. 3 the register block 208 of the audio-interface IC 102 which employs the preferred analog multiplexer 412 includes a software programmable bit 208ca3 located in the low-order byte of the Electrical Power Register 208c. Setting bit 208ca3 activates software data control of the multiplexer control 414 via the SMBus 162, while resetting bit 208ca3 activates hardware enablement of the multiplexer control 414 via the touchdown signal-line 418. To effect software disabling and enabling of the multiplexer control 414, the low-order byte of the Electrical Power Register 208c also includes a software programmable bit 208ca2 which if set enables operation of the transmission gates 402 and 406. Conversely, resetting bit 208ca2 disables operation of the transmission gates 402 and 406.

When the computer 100 is fully operational with a device connected to the IDE-bus extension 129, using one of the manual input devices 112 a user of the computer 100 may direct a computer program executed by the CPU 120 to release the device connected to the IDE-bus extension 129. In response thereto, the computer 100 causes the analog multiplexer 412 to isolate the IDE-bus extension 129 from the remainder of the CD-ROM subsystem 106, and then mechanically releases the device so it may be physically removed from the computer 100. Upon subsequent insertion of a device into the computer 100 and connection of that device to the IDE-bus extension 129, the device becomes mechanically locked into the computer 100, and the audio-interface IC 102 responsive, either the hardware signal or software data, re-couples the IDE-bus extension 129 to the remainder of the CD-ROM subsystem 106 thereby restoring the computer 100 to full operation.

1 Although the present invention has been described in terms of the presently preferred
2 embodiment, it is to be understood that such disclosure is purely illustrative and is not to be
3 interpreted as limiting. Consequently, without departing from the spirit and scope of the
4 invention, various alterations, modifications, and/or alternative applications of the invention will,
5 no doubt, be suggested to those skilled in the art after having read the preceding disclosure.
6 Accordingly, it is intended that the following claims be interpreted as encompassing all
7 alterations, modifications, or alternative applications as fall within the true spirit and scope of the
8 invention.